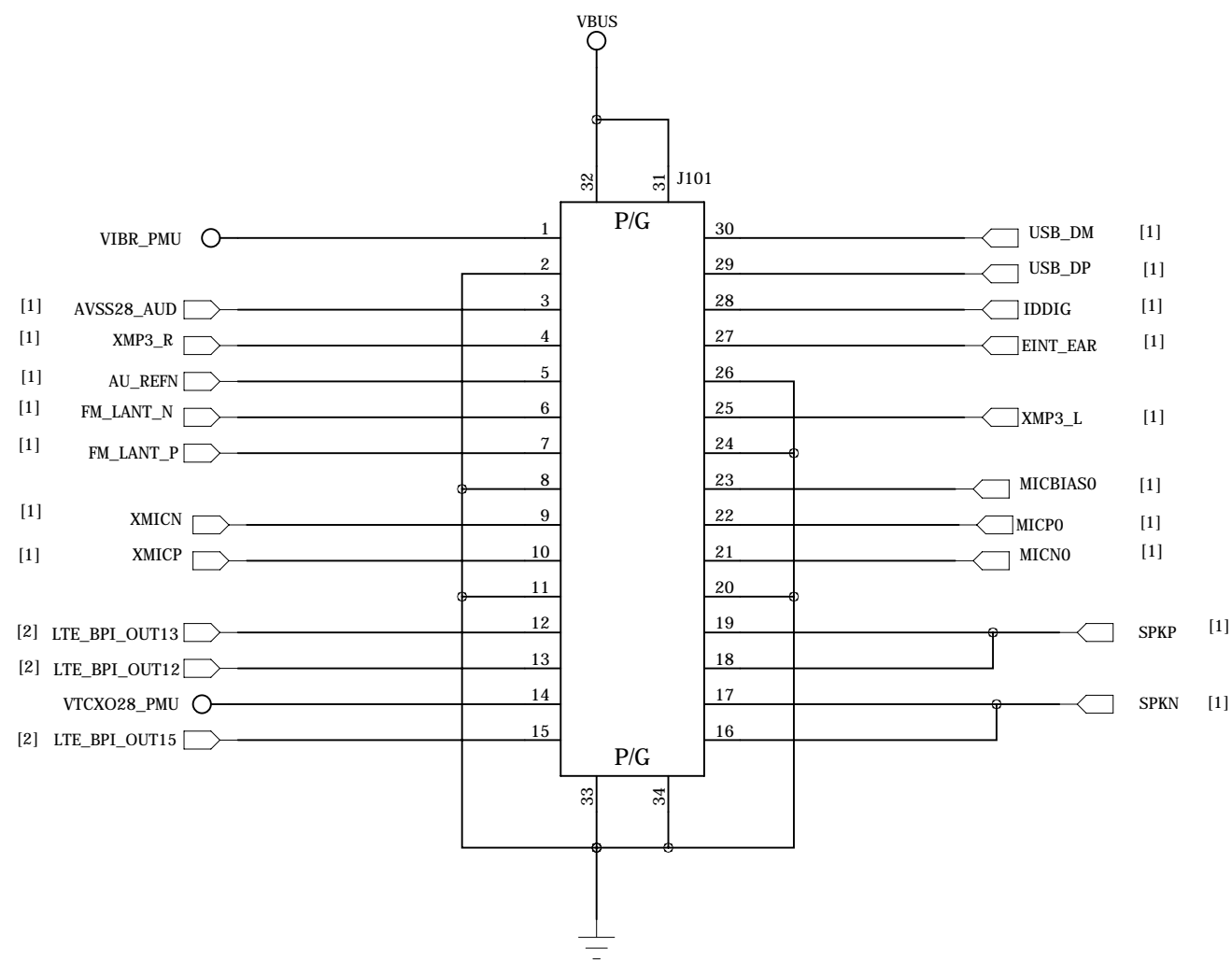


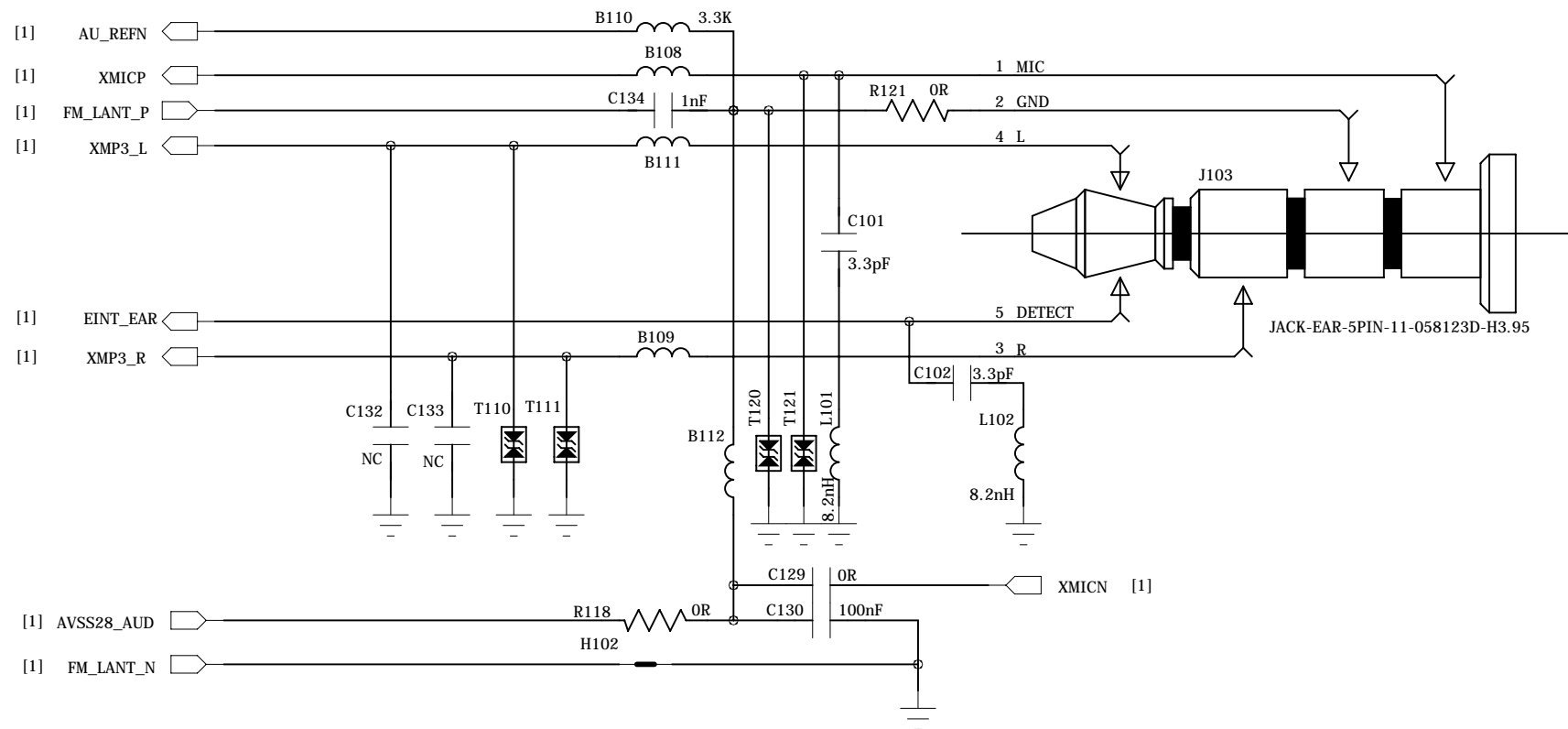
EXCHANGE IO

The diagram illustrates the EXCHANGE IO interface, showing the connection between a central P/G (Power/Ground) block and various external components. The components are connected to the P/G block via pins 1 through 34. The connections are as follows:

- Pin 1:** VIBR_PMU
- Pin 2:** AVSS28_AUD [1]
- Pin 3:** XMP3_R [1]
- Pin 4:** AU_REFN [1]
- Pin 5:** FM_LANT_N [1]
- Pin 6:** FM_LANT_P [1]
- Pin 7:** XMICN [1]
- Pin 8:** XMICP [1]
- Pin 9:** LTE_BPL_OUT13 [2]
- Pin 10:** LTE_BPL_OUT12 [2]
- Pin 11:** VTCX028_PMU
- Pin 12:** LTE_BPL_OUT15 [2]
- Pin 13:** USB_DM [1]
- Pin 14:** USB_DP [1]
- Pin 15:** IDDIG [1]
- Pin 16:** EINT_EAR [1]
- Pin 17:** XMP3_L [1]
- Pin 18:** MICBIAS0 [1]
- Pin 19:** MICP0 [1]
- Pin 20:** MICN0 [1]
- Pin 21:** SPKP
- Pin 22:** SPKN
- Pin 23:** VBUS
- Pin 24:** Ground
- Pin 25:** Ground
- Pin 26:** Ground
- Pin 27:** Ground
- Pin 28:** Ground
- Pin 29:** Ground
- Pin 30:** Ground
- Pin 31:** Ground
- Pin 32:** Ground
- Pin 33:** Ground
- Pin 34:** Ground



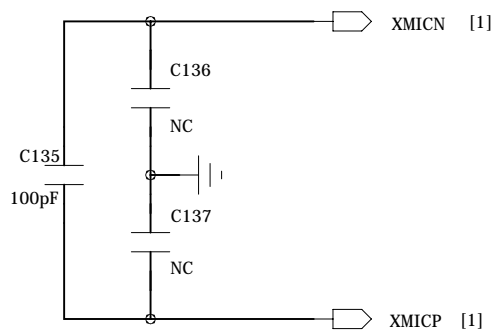
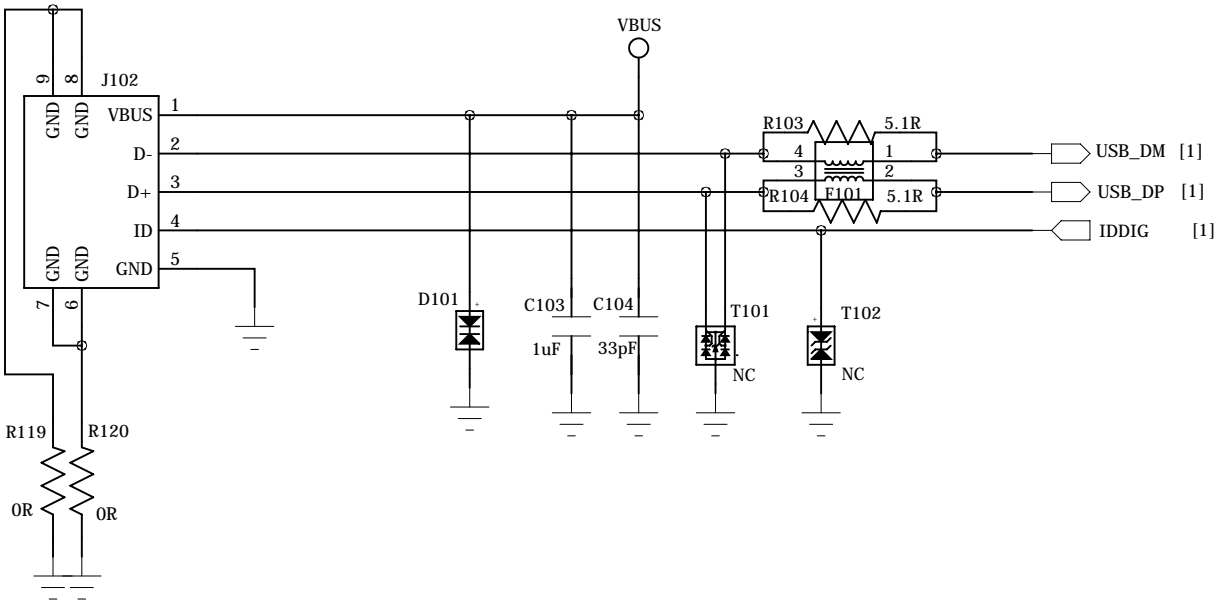
3.5mm Audio Jack



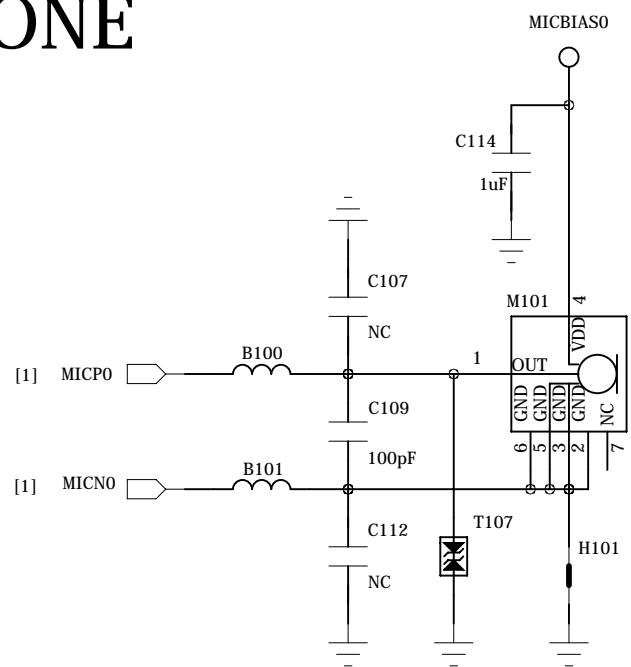
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Earphone MICPHONE

Schematic diagram of the Earphone MICPHONE circuit. The circuit shows a common ground connection on the left. Two output lines, XMICN [1] and XMICP [1], are connected to the top and bottom of a central component. This central component consists of a series of capacitors and a diode: C136 (top), NC (No Connection), a diode (pointing right), C137, and NC (bottom). A 100pF capacitor, labeled C135, is connected between the common ground and the top of the central component.

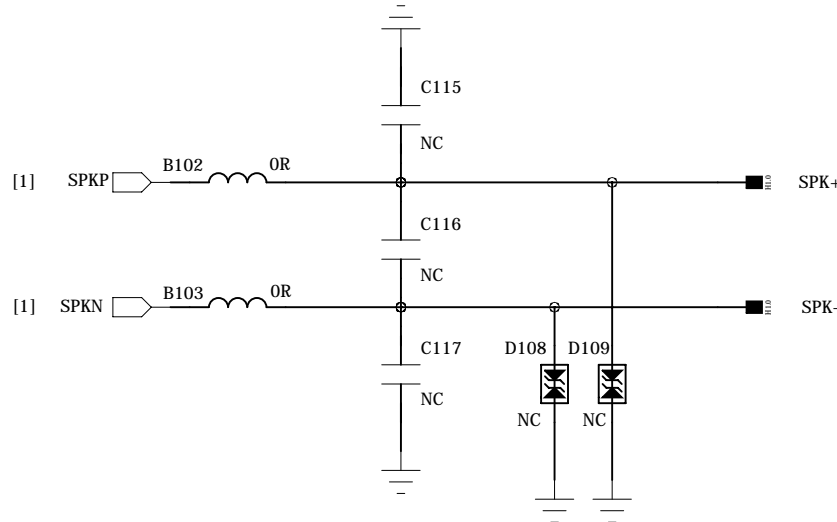
[illegible]

Main MICROPHONE



SPK

The schematic diagram illustrates the SPK (Speaker) circuit. It features two input lines, SPKP and SPKN, each connected to a speaker terminal (B102 and B103) via an OR gate. The outputs of the OR gates are connected to the speaker terminals. The circuit also includes two diodes, D108 and D109, connected to ground. Capacitors C115, C116, and C117 are connected to ground. The circuit is labeled with 'SPK+' and 'SPK-' at the output terminals.



VIB

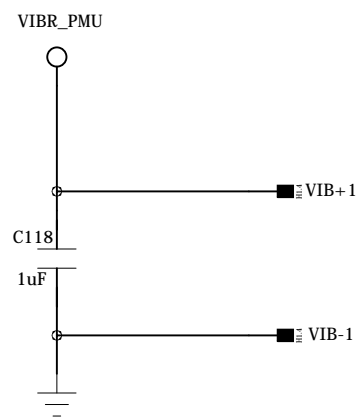
VIBR_PMU

C118

1uF

VIB+1

VIB-1



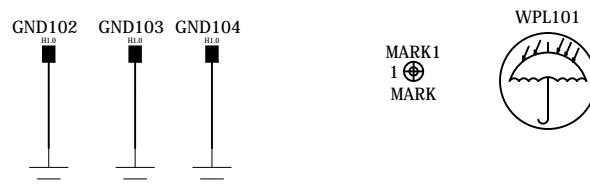
$\mathbb{D}_i \circ \dot{a} \frac{1}{2} \dot{O}_\mu \emptyset_\mu^- \mathbb{A} \neg$

GND102 GND103 GND104

MARK1
 1
 MARK

WPL101

Modified Date: 2018/5/21



COMPANY: TRANSSION HOLDINGS				MODEL: H621		Modified Date: 2018/5/21	
DRAWN	DZN/TS	DATED	20180521	TITLE: 1.AUDIO_IO_VIB_RF		VERSION: V1.2	SHEET: 1 OF 2
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

